

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi KOBAYASHI et al.

Appln. No.:

Filed: Herewith

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
documents listed on the attached Form PTO-1449 are hereby
cited.

The documents on the attached List were cited in the
specification, on pages 1, 2, 28, and 35, and their
relevance is indicated therein.

Respectfully submitted,

MWS:lat

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By: 
Mitchell W. Shapiro
Reg. No. 31,568

November 24, 2003

FORM PTO-1449				Atty. Docket No. XA-9997		Appln. No.	
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>							
				Applicant Takashi KOBAYASHI et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	2001-028428	1/30/01	Japan			Abstract
	AK	2001-085541	3/30/01	Japan			Abstract
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							